

Gigabit ethernet

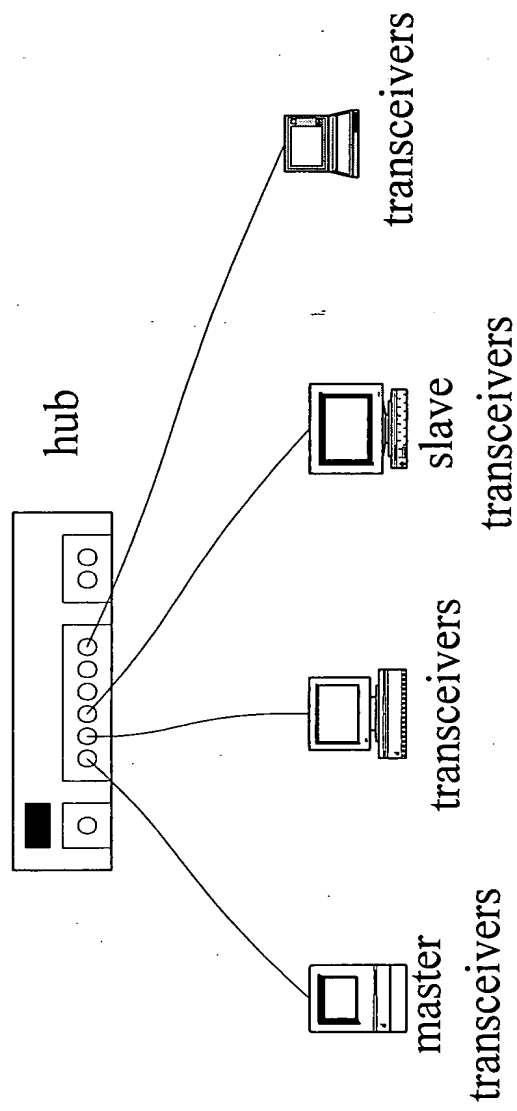


FIG. 1A

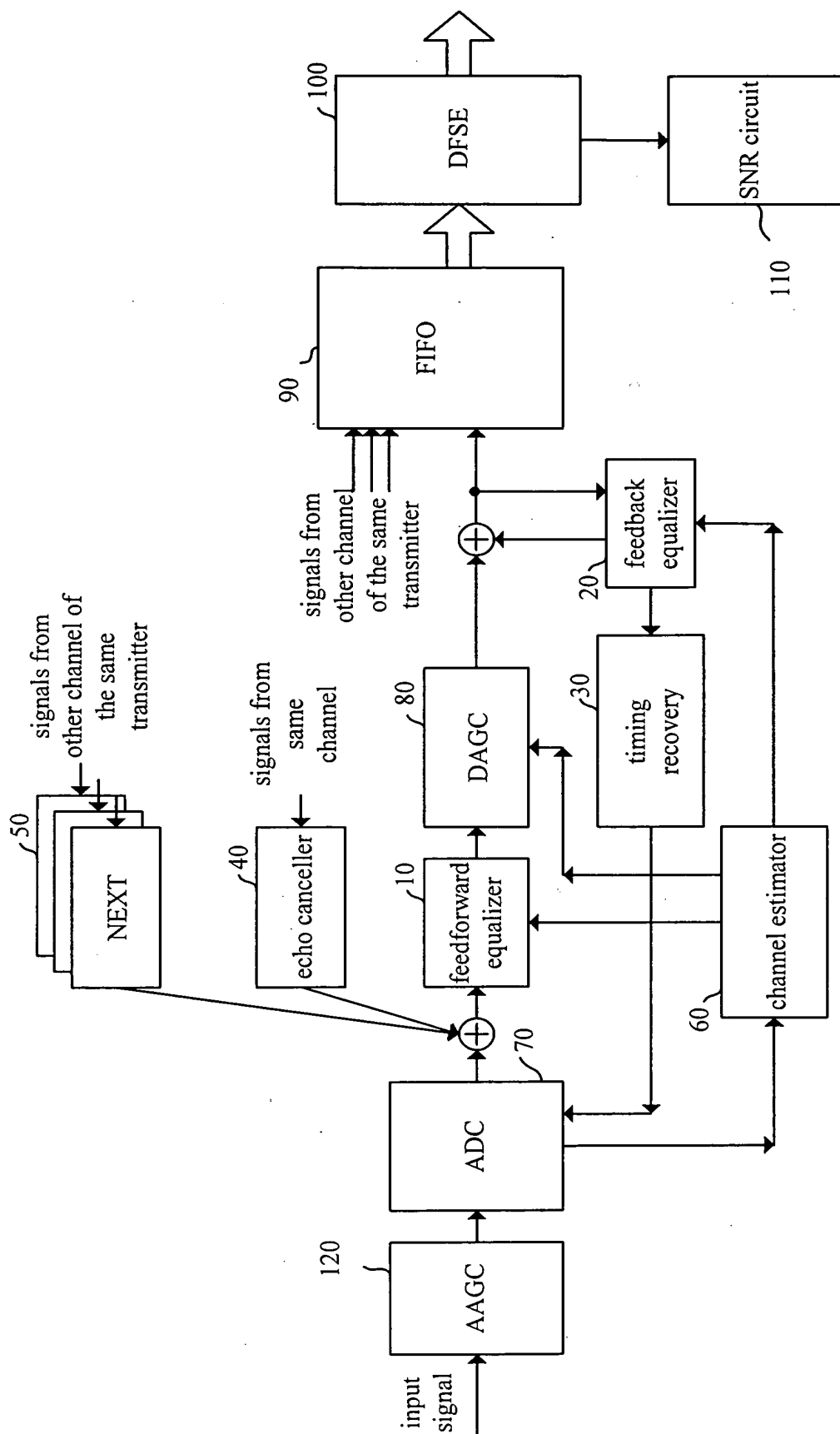
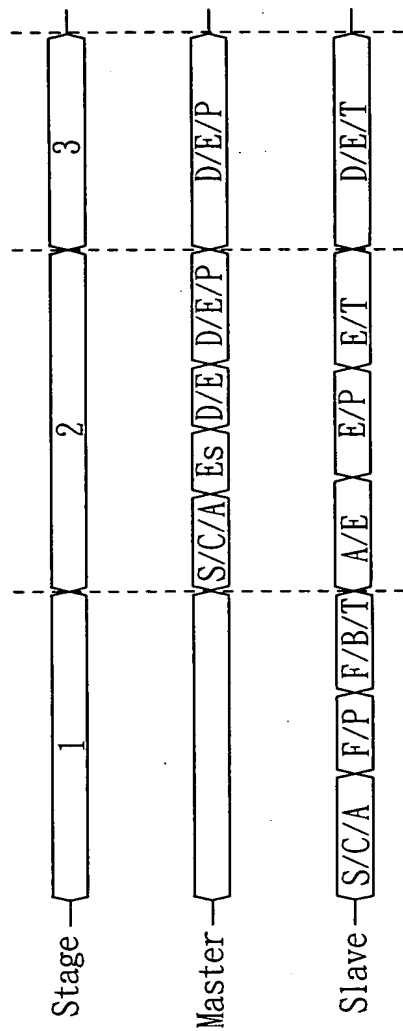


FIG. 1B



S : Signal Detection

F : FFE

B : FBE

D : FFE + FBE

P : Phase Loop of Timing Recovery

T : Frequency Loop and Phase Loop of Timing Recovery

E : Echo and NEXT Loop

C : Chennel Estimation

Es: Echo Estimation

A : AAGC

FIG. 2

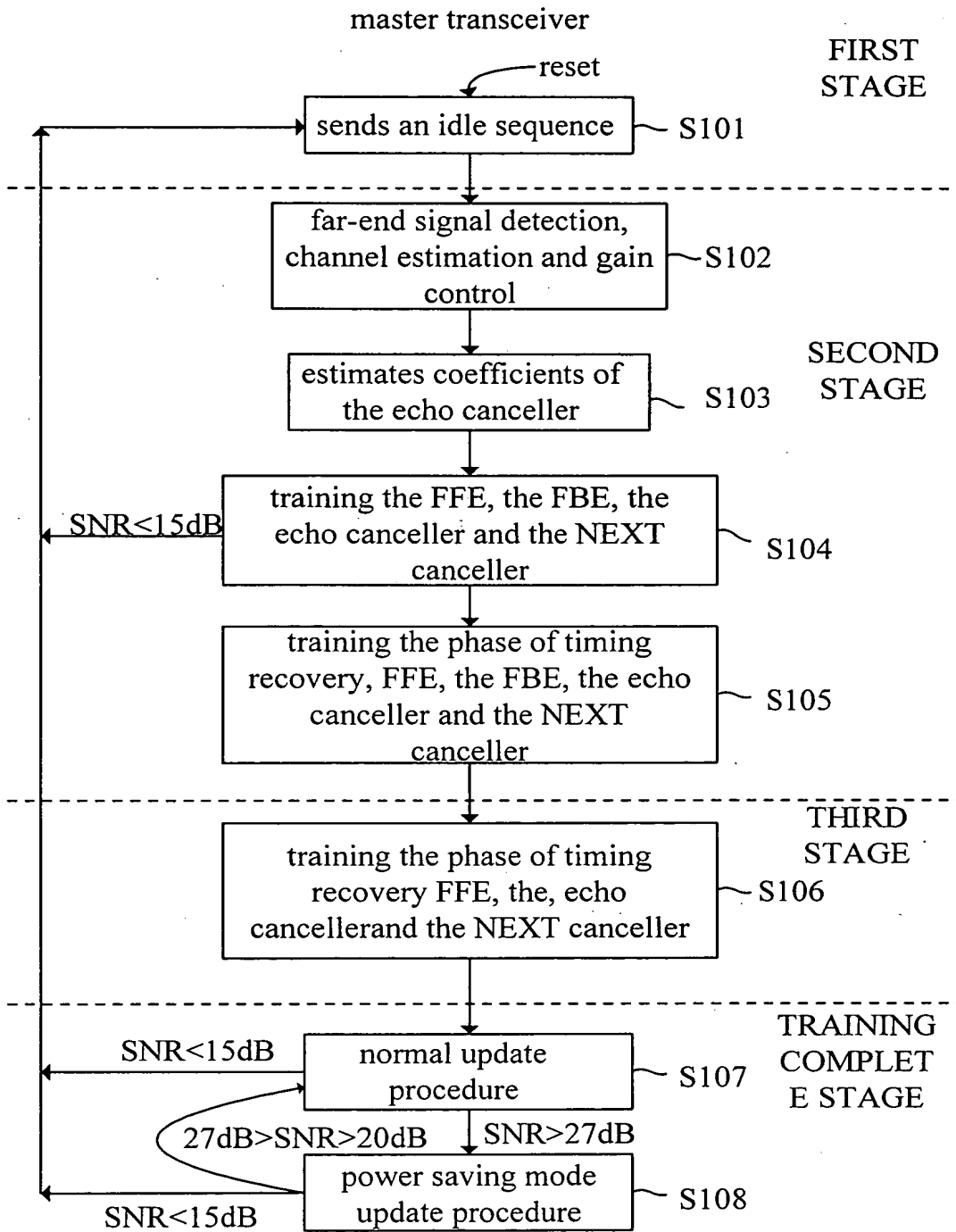


FIG. 3

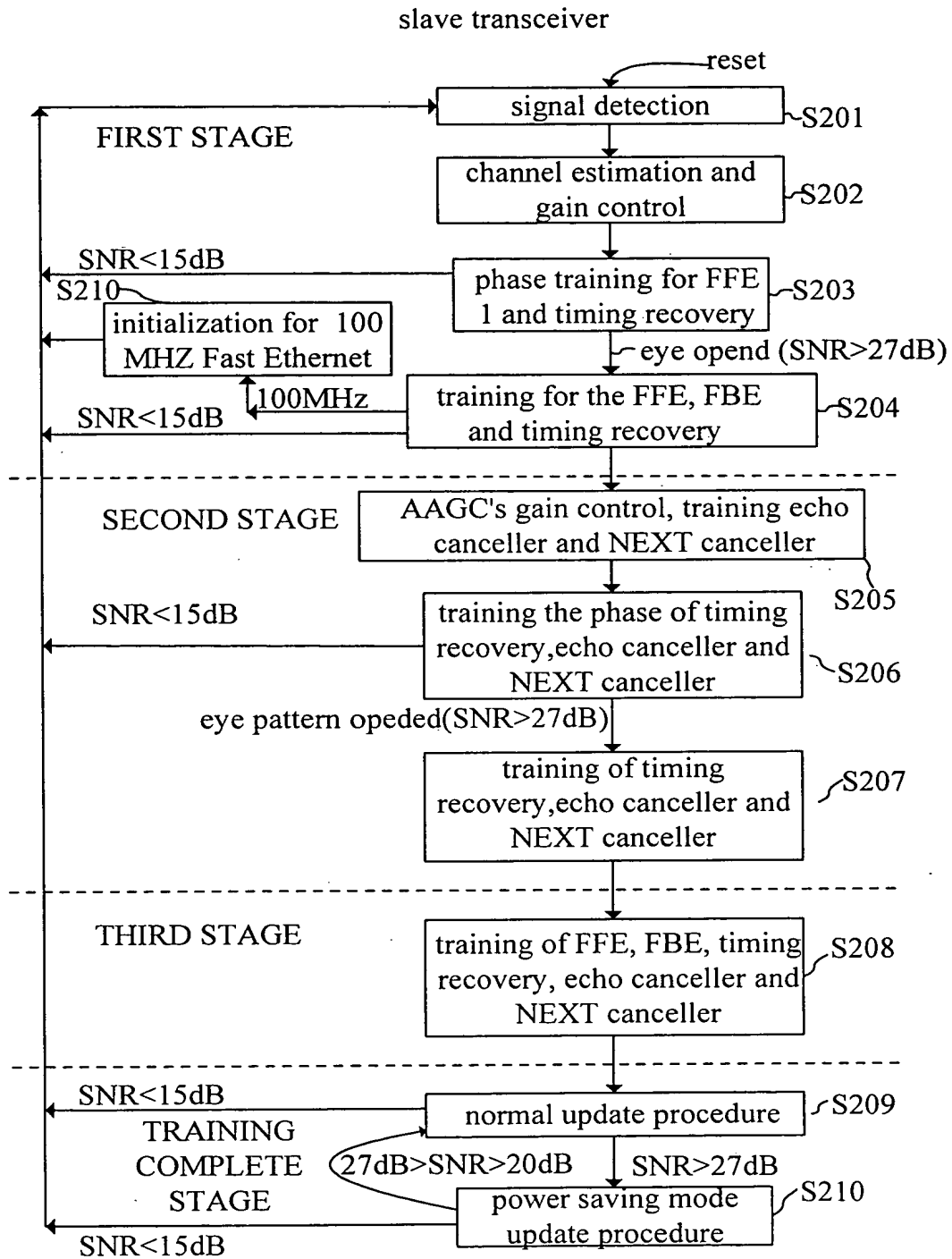


FIG. 4

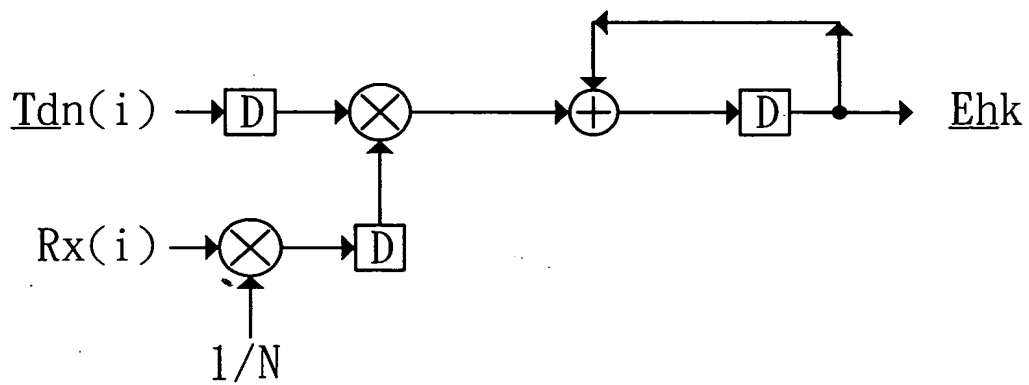


FIG. 5

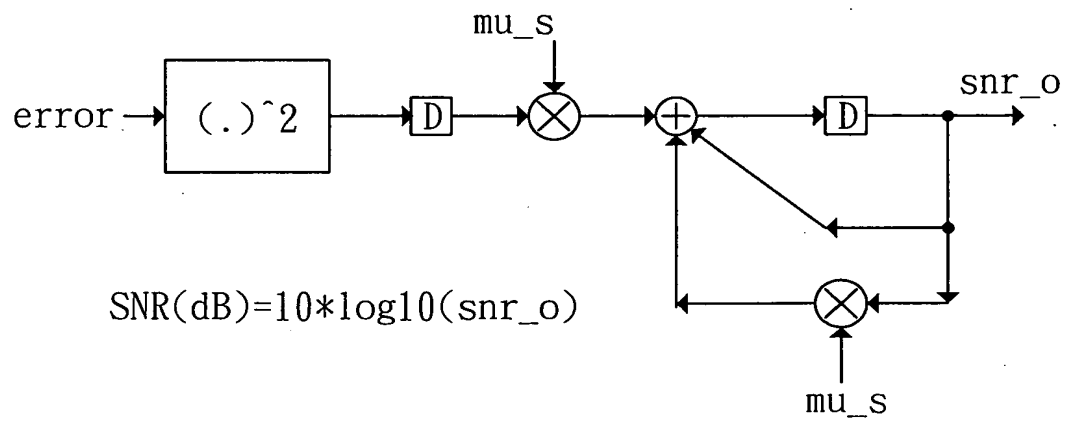


FIG. 6